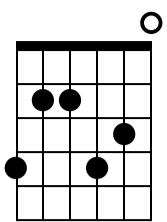
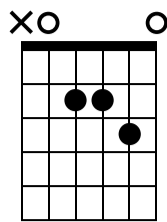




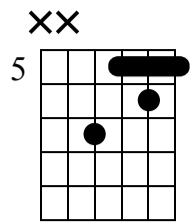
**E7/G#**



**Asus4**



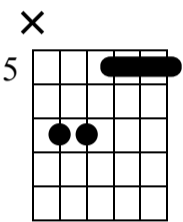
**F/A**



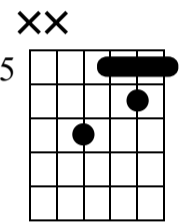
10 *let ring* *let ring* *let ring* 11 *let ring* *let ring* 12 *let ring* *let ring* *let ring* *let ring*

T	3	3	3	3	0	3	1	0	1	0	1	3	5	6	5	6	6	5	8
A	2	4	4	2	2	2	2	2	2	2	2	2	7	5	6	5	6	5	7
B	4			0	4	0	0						8		0			7	5

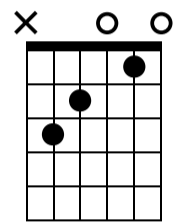
**Am/E**



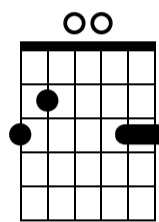
**F/A**



**C**



**G**



13 *let ring* *let ring* *let ring* *let ring* 14 *let ring* *let ring* *let ring* *let ring* 15 *let ring* *let ring*

T	5	5	5	0	1	3	5	6	5	6	5	6	5	8	0	0	1	1	3	3	3	0
A	7	5	5	7	2	2	5	6	6	5	6	5	7	5	0	0	0	0	0	0	0	0
B	7	7	0	0	2	2	0	8	0	0	7	7	5	3	3							3

16 *let ring* *let ring* 3x

T	1	1	0	1	0	5
A	0			0		
B	3			3		7